

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	9	synthesizer and "row address" and reset same transition same detect\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 09:40
L9	83	kang.in. and address near5 latch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 10:08
L10	4227	reset same signal same transition same detect\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 10:09
L11	1566	reset same signal same transition near3 detect\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 10:09
L12	688	reset near3 signal same transition near3 detect\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 10:09
L13	358	reset near3 signal with transition near3 detect\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 10:10
L14	191	reset near3 signal with transition near3 detect\$3 same control\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 10:10
L15	159	reset near3 signal with transition near3 detect\$3 same control\$4 and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 10:11
L16	175	reset adj signal with transition near3 detect\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 10:20
L17	97	reset adj3 transition adj detect\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/03 10:21

S1	11	(("6067244") or ("6272594") or ("6301145") or ("6314016") or ("6363439")).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/30 10:25
S2	1747	(365/145).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/23 08:01
S3	3554	(365/230.03).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/23 08:01
S4	1077	(365/185.11).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/23 08:01
S6	3666	ferroelectric and page	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/23 09:06
S7	7	ferroelectric and "page address" same "row address"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/23 09:19
S8	64	ferroelectric and page adj mode	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/23 09:06
S9	35	ferroelectric and "page address"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/23 09:43
S10	10	ferroelectric and "page address" and "row address"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/23 10:01
S12	3	"block page address" and "column page address"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 10:28

S13	83	ferroelectric and bank near5 address and row near5 address and column near5 address	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 10:51
S14	2	ferroelectric same bank same row same column same bit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 10:43
S15	425	(711/209).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/30 10:35
S16	696	ferroelectric same architecture	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 10:36
S17	20	ferroelectric same architecture near5 (bank block)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 10:40
S18	16	horiguchi.in. and spare.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 10:41
S20	16	horiguchi.in. and spare.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 10:41
S21	617	sDRAM same bank same row same column same bit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 10:43
S22	44	sDRAM same bank same row same column same bit same page	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 10:43
S23	73	ferroelectric and bank same row same column same bit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 10:45

S24	18	ferroelectric and (configuration configur\$3) same bank same row same column same bit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 10:49
S25	10	row same address same column near block and ferroelectric	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 10:49
S26	197	ferroelectric and (bank block) near5 address and row near5 address and column near5 address	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 11:01
S27	223	ferroelectric and page same address	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 11:02
S28	15	ferroelectric and page near3 address same "row address"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 11:04
S29	20	ferroelectric and "block address" same "row address"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 11:39
S31	5	("5596543" "5909407").PN. OR ("6525989").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 11:40
S32	3	("4926384" "5335206" "6525989").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 11:47
S33	238	"block address" same "row address" same a\$2 same bit	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 11:50
S34	16	"block address" same "row address" same a\$2 same bit same (position location)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 11:51
S35	2	("5619456" "5822254").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 12:14
S36	53	"address buffer" near5 latch and ferroelectric	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 12:20
S37	24003	"address buffer" near5 "flip flop"and ferroelectric	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 12:20

S38	1	"address buffer" near5 "flip flop" and ferroelectric	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 12:20
S39	73	"address buffer" near5 "flip flop"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 12:33
S40	27	address near3 buffer near5 latch same decoder same "chip enable"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 12:39
S41	126	row near3 address near3 transition near3 detect\$3	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 12:40
S42	9	row near3 address near3 transition near3 detect\$3 same (block bank) adj address	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 12:40
S43	19	row near3 address near3 transition near3 detect\$3 and ferroelectric	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 12:41
S44	11	row near3 address near3 transition near3 detect\$3 and ferroelectric and "chip enable" and decoder and address adj (buffer latch)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 13:11
S45	12	row near3 address near3 transition near3 detect\$3 and ferroelectric and "chip enable" and decoder and address adj (buffer latch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 13:11
S46	12	"block architecture" same memory and ferroelectric	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 13:47
S47	232	"block architecture" same memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 13:55
S48	15	"block architecture" same memory and (sub-block subblock)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 13:57
S49	316	mcclure.in. and buffer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 13:58

S50	105	mcclure.in. and buffer and pad	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 13:58
S51	2	("5619456").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/30 14:03
S53	3267	column near page	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 14:03
S54	22	column near page and ferroelectric	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 14:11
S55	37	kuroda.in. and ferroelectric	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 14:07
S56	214	column near page same block	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 14:12
S57	1240	memory same architecture same hierarchy	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 14:12
S58	1	memory same architecture same hierarchy same block same (subblock sub-block)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/30 14:14
S59	23	("3629612" "4768172" "4984201" "5093809").PN. OR ("5170375").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 14:25
S60	42	Memory same architecture same multilevel same hierarchy	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 14:38
S62	478	hierarch\$4 and ferroelectric	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 14:37

S63	2	multilevel same hierarch\$4 and ferroelectric	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 14:37
S64	5878	Memory same (architecture structure) same hierarch\$4	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 14:38
S65	108	Memory same (architecture structure) same hierarch\$4 and ferroelectric	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 14:38
S66	69	Memory near5 (architecture structure) same hierarch\$4 and ferroelectric	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 14:41
S70	63	abedifard.in.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 14:44
S71	63	abedifard.in. and flash	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/30 14:44
S72	10751	column same block same row same address	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/01 20:34
S73	100	"column block" same "row block" same address	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/01 20:36
S74	12	ferroelectric and row near5 address near5 transition near5 detect\$3 and ("chip enable") and address near3 (buffer latch register) and decod\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/01 20:38
S75	410	(711/117).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/01 21:19
S76	480	(365/238.5).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/01 21:19
S77	645	(365/233.5).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/01 21:19

S78	9	S76 and ferroelectric	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/01 21:29
S79	20	S76 and S77	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/01 21:29